

The diagram illustrates a multi-resolution image processing system. It features three parallel processing paths, each starting from an 'ORIGINAL HIGH RESOLUTION IMAGE' (labeled 5, 5', and 5''). Each path includes a 'DOWNSCALE' operation leading to a processing block (labeled 10, 10', and 10''). These blocks are interconnected by a central dashed line, which also passes through an 'UP-SCALE' block (labeled 20). The paths then converge through a series of intermediate blocks (labeled 30, 30', and 30'') and finally output to a single high-resolution image (labeled 40). The diagram uses rectangles with diagonal hatching to represent images at different resolutions.



REPLACEMENT SHEET

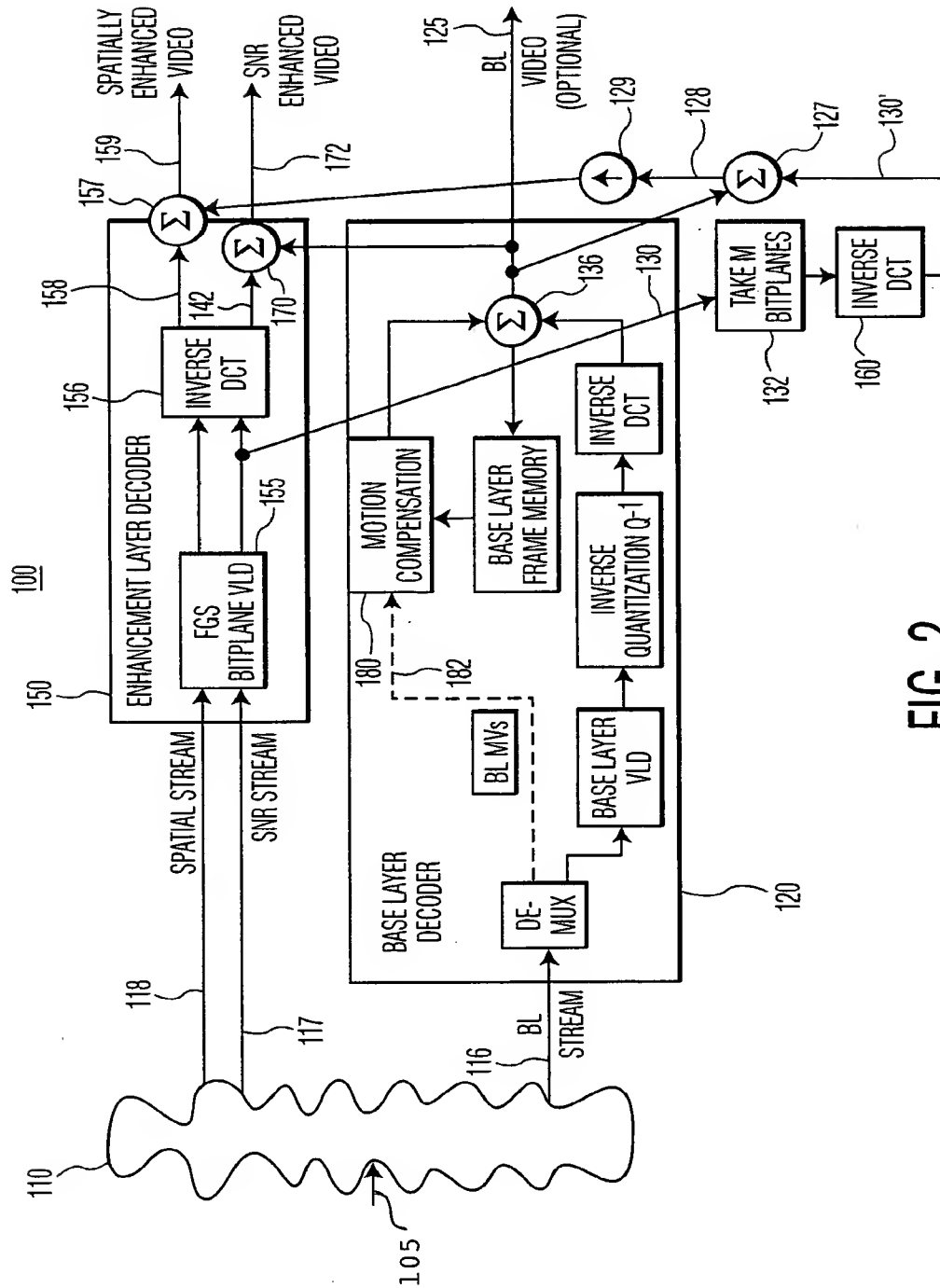


FIG. 2